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Control Law Synthesis for Distributed Multi-Agent Systems: Application to Active Clock Distribution Networks

A. Korniienko, G. Scorletti, E. Colinet, E. Blanco, J. Juillard, D. Galayko

Abstract — In this paper, the problem of active clock distribution network synchronization is considered. The network is made of identical oscillators interconnected through a distributed array of phase-locked-loops (PLLs). The problem of the PLL network design is reformulated, from a control theory point of view, as a control law design for a distributed multi-agent system. Inspired by the decentralized control law design methodology using the dissipativity input-output approach, the particular topology of interconnected subsystems is exploited to solve the problem by applying a convex optimization approach involving simple Linear Matrix Inequality (LMI) constraints. After choosing the dissipativity properties which is satisfied by the interconnection matrix, the constraints are transformed into an H_∞ norm constraint on a particular transfer function that must be fulfilled for global stability. Additional constraints on inputs and outputs are introduced in order to ensure the desired performance specifications during the H_∞ control design procedure.

I. INTRODUCTION

THE active clock distribution network (Fig.1) can be used as an alternative way to distribute the clock in synchronous many-core microprocessor systems. It has a large number of advantages in terms of perturbation rejection, robustness properties and power consumption [1-6]. In these systems, synchronization is crucial to ensure a correct system operation. Additionally, it is required that the generated clock signal is slightly sensitive to the external perturbations: temperature, noise and power, *i.e.* it possesses some signal purity properties. The network is generally composed of identical, independent voltage controlled oscillators (VCOs represented with dots in the right part of Fig. 1) that are spatially separated and connected in a two dimensional regular grid with phase detectors (PDs, represented with rectangles in Fig 1) which are located in between adjacent VCOs in order to compensate the clock propagation delays; and identical filters (for sake of cleanness, represented by F only in the left part of Fig 1). In general, each oscillator can be followed by a frequency divider ($1/d$ in the left part of Fig. 1) in order to distribute lower frequency clock signal throughout the network. The overall network is modelled as an interconnection of multiple input phase locked loop (PLL) nodes represented in the left part of Fig.1 with m_i shared phase detectors. In general, PLLs can be digital (All Digital PLLs) [4,7]. Depending on the application, one or several reference inputs can be introduced in the network. These reference oscillators must be synchronized and serve to enforce a common network clock frequency. Dynamically speaking, the described system has a much more complex behaviour than conventional clock distribution network. It is actually an automatic control system made of several feedback loops and caution must be taken to ensure stability of the overall system. Due to its complexity, classical microelectronics methods and tools fail to analyze and efficiently design this class of systems. In this paper, we illustrate how to apply control theory tools in order to ensure appropriate working operation of the whole system.

Methods for designing a stand-alone PLL are well-known in the field of microelectronics [8,9]. From a control system point of view, a PLL represents an LTI feedback dynamical system in the phase domain and one can design quite easily a controller ensuring good stability margins and performance requirements using standard control theory tools [8-11]. However, there is no guarantee for the global network that it convergences to the “synchronous state” even though each node is identical and properly designed to ensure the local convergence on an average input signal. There is no guarantee neither that the performance will not be degraded by the network. These aspects of global network inter-connection and nodes coupling are very important and must be taken into account during the system design procedure.

One of the most important results in understanding of global network dynamic or, more-generally, behavior of networked multi-agent systems is the work [12] where the authors give a necessary and sufficient stability condition for such LTI network. They use a transformation of the Laplacian interconnection matrix allowing the expression of the global stability test in the form of simultaneous stability tests of N independent systems that have different feedback gains. Since these gains are defined by Laplacian eigenvalues (in general complex), one may apply in straight manner the Nyquist stability test of the global network. The paper [12] gave rise to a large number of various papers that use the same transformation to specify the stability test based on variety of approaches such as passivity, \mathcal{L}_2 gain [13-18], more general IQC characterization [19,20]. These papers deal with different set of additional problems such as nonlinear interconnections, effects of delay propagation and structure switching, but all of them are tools allowing only the stability analysis. However, there are two extensions to the design of a control law based on the result of [12].

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The first one is the robust control design pointed out in [12, 21, 22]. Using the Gershgorin theorem [23], authors propose to characterize the domain where all eigenvalues of the interconnection matrix must lie. Thereafter, they propose to express the feedback gain resulting from the transformation in the form of an uncertainty characterized by a certain norm. Further, one can apply a local robust control design guarantying the stability for all possible complex uncertainties using such a norm description and as a consequence ensuring the stability for all associated interconnection topologies. This proposed approach may be a suitable solution for the design problem. Nevertheless, the norm constraint that has to be guaranteed for the global stability is extremely conservative. It is actually based on a norm constraint on the complementary sensitivity function T that should have a maximum transmission gain not greater than one. As it is well-known [24], the maximum value of the complementary sensitivity function magnitude is bigger or equal to 1. So far, there is no successful implementation in the literature of such approach guaranteeing stability and desired performance in the same time for a multi-agent system.

The second extension is based on the simultaneous stabilisation of the N independent systems according to the Theorem 3 in [12]. The authors of [25] use the static state feedback control law as a solution of an LMI optimisation problem. It is only possible in the case when the relative states are available for the control. Otherwise, the observed based solution is used in [26-28]. However there is no efficient method proposed to compute the control law that ensures stability as well as performance criteria besides a convergence rate to the steady-state (synchronous or consensus state). Moreover, for the control law of [26-28], a communication network able to transmit arbitrary information (controller and observer states) is required.

In the present paper, we propose a new approach that ensures the overall system stability as well as performance criteria. Additionally, the proposed control law requires only a local system output relative exchange (relative output errors) and is the solution of a convex LMI optimisation problem.

It turns out that in the literature of decentralized control, the problem of control law design for interconnected subsystems has been deeply treated. The authors of [29] solve this issue using the input-output approach. They use a dissipativity characterization [30] of subsystems which is more general than the passivity or L_2 gain as in [13-18]. They solve afterwards LMI conditions of local controllers existence that ensure the global stability and performance. However, since the subsystems that were considered in [29] are not necessary identical, the complexity of the condition they use depends on the overall number of network nodes. This potentially leads to some computational problem difficulties because a set of LMI conditions has to be ensured for every single node. On the other hand in practice, depending on the problem, the subsystems to control are very often identical at least with the same structure, as in our application of PLLs synchronization. Reasonably speaking, this property should be exploited to release some constraints on the controller design procedure.

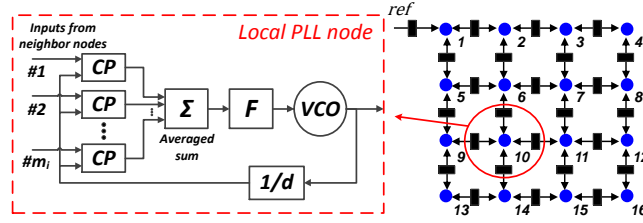


Fig. 1. Active clock distribution network

In the present paper inspired by the decentralized control in [29], we propose a design method that exploits the fact that nodes distributed in the network are identical. To do so, the dissipativity condition guaranteeing the overall network stability is transformed to an H_∞ constraint on a transfer function involving only one node dynamic. It significantly reduces the computational complexity of the filter design algorithm. By applying an H_∞ controller design ensuring this constraint to one single node, one can thus guarantee the stability of the overall network with specified condition on the interconnection matrix. Furthermore, we make sure that the performance constraints are locally fulfilled by the controller and we test afterwards possible performance degradation due to the interconnection.

The paper is organized as follows: the second section gives the used notations and preliminaries, in the third section we describe the general problem formulation. The main result is presented in the fourth section of the paper and its numerical application in fifth. We conclude the paper by the conclusion and the perspectives.

II. NOTATION AND PRELIMINARIES

Definition 1: A stable LTI causal operator T with input r and output φ is *strict $\{X, Y, Z\}$ -dissipative*, if there exist a real $\varepsilon > 0$, real matrices $X = X^T \leq 0, Y, Z = Z^T \geq 0$ such that, $\begin{bmatrix} X & Y \\ Y^T & Z \end{bmatrix}$ is a full rank matrix and with $\varphi = T(r)$:

$$\begin{aligned} \int_0^{+\infty} \begin{bmatrix} r(t) \\ \varphi(t) \end{bmatrix}^T \begin{bmatrix} X & Y \\ Y^T & Z \end{bmatrix} \begin{bmatrix} r(t) \\ \varphi(t) \end{bmatrix} dt &\leq -\varepsilon I \\ \Leftrightarrow \begin{bmatrix} I \\ T(j\omega) \end{bmatrix}^* \begin{bmatrix} X & Y \\ Y^T & Z \end{bmatrix} \begin{bmatrix} I \\ T(j\omega) \end{bmatrix} &\leq -\varepsilon I, \forall \omega \in \mathbb{R} \end{aligned} \quad (1)$$

If the inequality (1) is satisfied with $\varepsilon = 0$, the operator is thus called *$\{X, Y, Z\}$ -dissipative*.

\star denotes *Lower Fractional Transformation (LFT)*, for $H = \begin{bmatrix} H_{11} & H_{12} \\ H_{21} & H_{22} \end{bmatrix}$ it is defined as:

$$(H \star G)(s) = H_{11}(s) + H_{12}(s)G(s)(I - H_{22}(s)G(s))^{-1}H_{21}(s)$$

\otimes denotes the *Kronecker product* of two matrices defined by $A \otimes B = [a_{ij}B]$.

The H_∞ norm of a stable LTI system T is the maximal singular value of its transfer function that is formally defined as: $\|T\|_\infty = \sup_{\omega \in \mathbb{R}^+} \bar{\sigma}(T(j\omega))$.

III. PROBLEM STATEMENT

Let us consider the network of Fig. 1 where each node should be synchronized with its neighbours. The problem that has to be solved can be expressed as “find a control law F that achieves the synchronization of the overall clock distribution network in a specified time for some fixed inter-connection structure and some specified clock signal purity”.

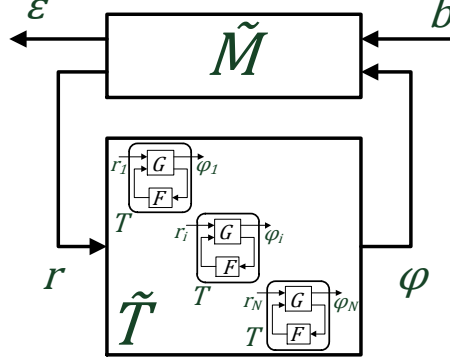


Fig. 2. Control law synthesis

To design the controller of a stand-alone PLL, one usually models it in the phase domain [8-10]. A periodic signal is represented by its phase growing as a ramp with certain speed corresponding to the instantaneous oscillator frequency. Then the phase domain model is linearized around its operation point. Supposing that PLL will work close to this trim point, the controller can be efficiently designed. The same procedure has to be performed in the context of coupled PLLs network too. This allows to have not only a valid LTI model of the network but also to do not take into account some types of issues such as an auto-sampling in the case of digital PLLs and mode-locking state which can be treated separately as in [31, 32] and [1,2,4].

The phase domain LTI model of the overall network is thus expressed as (2) and Fig.2.

$$\begin{cases} \varphi(t) = \tilde{T}r(t) \\ \begin{bmatrix} \varepsilon(t) \\ r(t) \end{bmatrix} = \begin{bmatrix} M_{11} & M_{12} \\ M_{21} & M_{22} \end{bmatrix} \begin{bmatrix} ref(t) \\ \varphi(t) \end{bmatrix} = \tilde{M} \begin{bmatrix} ref(t) \\ \varphi(t) \end{bmatrix} \\ \tilde{T} = I_N \otimes T = I_N \otimes (G \star F) \end{cases} \quad (2)$$

with $\varphi_i(t), r_i(t) \in \mathbb{R}^p, \forall i = 1..N$

In Fig.2, G represents the part of a node that has to be controlled, F the controller to be designed, \tilde{T} is the diagonal LTI matrix consisting of the N identical LTI models T and the block \tilde{M} is the interconnection matrix. Although in our application the subsystem T (elementary PLL node) is a single-input single-output (SISO) system, in order to obtain a result as general as possible, we cover MIMO case with blocks T of dimension $p \times p$. The vector r is the vector of input mean values of the nodes, φ is the vector of its outputs i.e. its local phase values, b is the vector of external inputs and ε is the vector of errors to be minimized depending on the performance specification. Indeed, the performance requirements can be expressed by the minimization of the H_∞ norm for the transfer function between b and ε . The mathematical expression of the performance objectives allows us not only to use the weighting transfer function approach for more precise frequency domain specifications, but also to generalize the problem to any problems of general networked multi-agent systems. Indeed, all engineering tasks such as: consensus, coordination, cooperation of subsystems can be expressed mathematically as an H_∞ norm less than a given level γ between some performance inputs and outputs [24] provided that the overall system is stable. In the following section we focus on this more general mathematical problem:

General problem formulation:

Given an LTI system G , an interconnection matrix \tilde{M} , find an LTI system F such that:

- 1) Each subsystem T separately as well as the overall network formation (2) is stabilized;
- 2) A given performance specification by restricting the H_∞ norm of local transfer function between performance inputs and outputs is ensured.

IV. MAIN RESULT

The described problem will be solved in two steps. First, a control law is designed such it ensures: a) the stability of each linear subsystem T and the overall system; b) an H_∞ norm less than a given level γ between some performance input and some performance output of the subsystem T . This level of performance is guaranteed only for an individual node i.e. the

“local” performance. As a consequence, the second step consists in testing the “global” performance (performance of a node relative to the overall network) degradation by the network interconnection.

Although the PLLs can have both digital and continuous elements (as in our application case), in this paper all digital blocks of each node are transformed into continuous time blocks by Tustin transformation so that the synthesis and analysis are performed in the continuous time domain. After the synthesis of the control law, an inverse continuous to digital time Tustin transformation necessary for the implementation is performed.

A. Stability

First, the problem of local and global stability is addressed. To do so, we focus on the loop represented in Fig.2 and (2) without any external inputs and outputs (ε, b) i.e. $M_{11} = 0$, $M_{12} = 0$, $M_{21} = 0$, $M_{22} = M$.

The main result of the paper can be summarized by the following theorem:

Theorem 1: Given the LTI system (2) and $p \times p$ real matrices $X = X^T \leq 0$, $Y, Z = Z^T \geq 0$,

If

1) there exists positive definite matrix $Q \in \mathbb{R}^{N \times N}$ such that the interconnection matrix M is $\{Q \otimes X, Q \otimes Y, Q \otimes Z\}$ -dissipative

2) $\|\hat{T}(j\omega)\|_\infty < 1$ (3)

with $\hat{T}(j\omega) = (-X)^{1/2}(T + X^{-1}Y)(Z - Y^T X^{-1}Y)^{-1/2}$

Then the overall system is stable.

Sketch of the proof (See [33] for the complete proof).

The proof is based on a graph separation argument [30, 34]. It is possible to prove the global stability of a feedback of two subsystems if they ensure two complementary input-output quadratic constraints.

The first constraint is ensured by the dissipativity condition 1) on the interconnection matrix M i.e. M is $\{Q \otimes X, Q \otimes Y, Q \otimes Z\}$ -dissipative.

The second constraint is actually ensured by the H_∞ norm constraint (3). Indeed, it can be proved that (3) implies the strict $\{-Q \otimes Z, -Q \otimes Y^T, -Q \otimes X\}$ -dissipativity of the global diagonal operator \hat{T} . It completes the proof.

Remark 1: The dissipativity condition on the interconnection matrix M is an LMI condition for some given $X = X^T \leq 0$, $Y, Z = Z^T \geq 0$:

$$\begin{cases} \exists Q \in \mathbb{R}^{N \times N}: Q = Q^T > 0 \\ \begin{bmatrix} I_{pN} \\ M \end{bmatrix}^T \begin{bmatrix} Q \otimes X & Q \otimes Y \\ Q \otimes Y^T & Q \otimes Z \end{bmatrix} \begin{bmatrix} I_{pN} \\ M \end{bmatrix} \leq 0 \end{cases} \quad (4)$$

As it is known, the feasibility problem of an LMI condition can be tested by applying the convex optimization algorithm [35, 36] which can be efficiently solved using e.g. Matlab LMI Toolbox. It is possible to transform (4) into the form of a quasi-convex optimization problem the solution of which allows to choose the X, Y, Z dissipativity characterization matrices. Further details can be found in [33].

Remark 2: The Theorem 1 constraint (3) can be ensured by H_∞ control law design see Fig.3. However, we exploit all possibilities of H_∞ design as MIMO design tools [24] by adding the desired performance inputs and outputs and by applying the design procedure for the extended system.

It should be mentioned that the stability problem reduces to solving the feasibility problem of two LMI conditions: the condition on the interconnection matrix M (4) and the condition on the H_∞ norm of the local transfer function (3). In the case when the state feedback control is possible, i.e. the controller has access to all plant states, the overall order of the LMI condition is equal to $3N + 2(n + p)$. n is the order of the subsystem G . The corresponding order of the simultaneous stabilization problem proposed in [25] is equal to $2Nn - n$. For a large number of multi-agents $N \geq 10$ with a plant order $n \geq 2$, the proposed stabilisation optimisation problem order is much smaller. Furthermore, it can solve the problems where the state feedback control is not possible.

B. Performance

The next step is the control law design ensuring some performance properties besides the stability constraints. For clarity of representation, the SISO case of node transfer function T , i.e. $p = 1$, is only considered. The equivalent approach can be applied to more general MIMO cases.

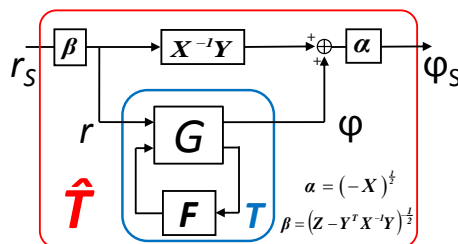


TABLE I
PERFORMANCE SPECIFICATIONS

Performance objective	Sensitivity function to be constrained	Corresponding weighted function
Perfect reference (ramp) tracking	$S(s) = (1 + G(s)F(s))^{-1}$	$(W_\varepsilon(s)W_r(s))^{-1}$
Fixed response time	$S(s)$	$(W_\varepsilon(s)W_r(s))^{-1}$
VCO input, output perturbations (noise, temperature) rejection	$GS(s) = G(s)(1 + G(s)F(s))^{-1}$ $FS(s) = F(s)(1 + G(s)F(s))^{-1}$	$(W_\varepsilon(s)W_r(s))^{-1}$ $(W_\varepsilon(s)W_b(s))^{-1}$ $(W_u(s)W_r(s))^{-1}$

Fig. 4. Extended system with weighted functions fixing desired performance

Usual performance specification from the microelectronic point of view and the control system point of view with corresponding sensitivity and weighting functions are presented in Table 1. By introducing additional performance inputs and outputs and related weighting functions to the scheme in Fig.3 the extended system description is deduced as in Fig.4.

The extended system in Fig.4 can be expressed as:

$$\tilde{P}(s) = \begin{bmatrix} W_\varepsilon(s)S(s)W_r(s) & -W_\varepsilon(s)GS(s)W_b(s) \\ W_u(s)FS(s)W_r(s) & -W_u(s)T(s)W_b(s) \\ W_r^{-1}(s)\hat{T}(s)W_r(s) & -W_r^{-1}(s)T_{rb}(s)W_b(s) \end{bmatrix} \quad (5)$$

with weighting functions $W_\varepsilon(s)$, $W_u(s)$, $W_r(s)$, $W_b(s)$ as a mathematical description of constraints with respect to the application.

The next step is the application of the H_∞ design giving us the controller that ensures the stability of the feedback system T (local stability of a node) and an H_∞ norm of the transfer function $\tilde{P}(s)$ less or equal to a given level γ . If in addition $\gamma < 1$ one thus obtains:

$$\begin{cases} |S(j\omega)| < |W_\varepsilon^{-1}(j\omega)W_r^{-1}(j\omega)| \\ |GS(j\omega)| < |W_\varepsilon^{-1}(j\omega)W_b^{-1}(j\omega)| \\ |FS(j\omega)| < |W_u^{-1}(j\omega)W_r^{-1}(j\omega)|, \forall \omega \in \mathbb{R} \\ |T(j\omega)| < |W_u^{-1}(j\omega)W_b^{-1}(j\omega)| \\ \|\hat{T}(s)\|_\infty < 1 \end{cases} \quad (6)$$

The algorithm of control law design can now be summarized as follows:

Algorithm of the filter design:

- 1) Choose the dissipativity characterization i.e. X, Y, Z and apply the LMI optimization algorithm to find a matrix Q that satisfies (4);
- 2) Select the weighting functions depending on the performance specifications;
- 3) Apply the H_∞ control design to the extended system (5) of Fig.4;
- 4) If a controller achieving $\gamma < 1$ is obtained then the local stability is guaranteed; the four first constraints in (6) ensure the local performance while the last one ensures the constraint (3) and thus by applying the Theorem 1 the global stability of the network.
- 5) If no controller is found or if the corresponding value of γ is high such that the constraint (3) is not satisfied, reduce the weighing function constraints i.e. performance requirements. And then go to the step 3.
- 6) By computing the global transfer function from performance input b to the performance output ε test how the global performance is degraded compared to the local performance ensured by (6).

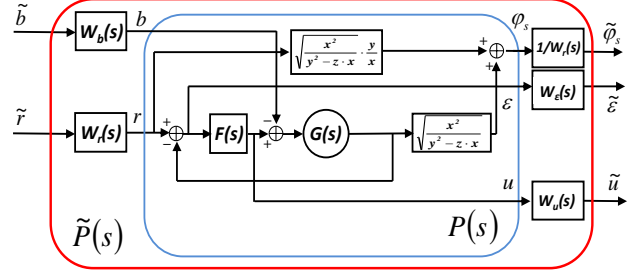
V. NUMERICAL EXAMPLE

For our application, a 4x4 Cartesian two dimensional ADPLL network is considered where the PLL at the upper-left corner has one additional external reference input as illustrated on Fig.1. The distribution network generates the clock at 0.97 GHz with a frequency dividing factor of $d = 4$. The total number of nodes (PLLs) is $N = 16$. The VCO is modelled as a pure integrator $G(s) = K_{vco}/s$ with gain $K_{vco} = 1.25 \cdot 10^6$ Hz/command units. The phase detectors are modelled by an adder followed by a gain $K_{pd} = 20.5$ error units/rad. The real m_i is the number of i^{th} PLL inputs. All nodes in the network are assumed to be identical.

The linearized model in Fig. 1 can be transformed into the form of Fig.2 described by (2) where M_{22} is the 16×16 normalized adjacency interconnection graph matrix [12] where i, j^{th} element is equal to $1/m_i$ if i^{th} node receives the information from j^{th} node and to 0 otherwise. The reference input is taken into account by m_i as an additional input i.e. for the first node $m_1 = 3$.

In addition the reference $ref(t)$, at the first node input with coefficient $1/m_1$, and first error output $\varepsilon(t) = ref(t) - \varphi_1(t)$, are introduced in order to enforce the reference tracking. This results on the following interconnection matrix bloc definition: $M_{11} = 1$, $M_{12} = [-1 \ 0 \ \dots \ 0]$ and $M_{21} = [1/3 \ 0 \ \dots \ 0]^T$ and the solution of the algorithm described in the previous section:

Fig. 3. Transformed model ensuring the global stability



1. For $\gamma = -1.2$, $\gamma = 0.12$, $z = 1$, we find Q satisfying (4)
2. Based on the performance specification, the weighting functions are chosen (see dotted red line in Fig.5)
3. Applying the H_∞ control design to the extended system (5) we obtain the performance level $\gamma = 0.99$ and PI controller:

$$F(s) = \frac{2.92(s+1.44 \cdot 10^5)}{s}$$

The corresponding sensitivity functions for the local PLL node as well as the performance for the transfer function of the first node in the overall PLL network are illustrated in Fig.5. As it can be seen the designed controller ensures:

- 1) The stability of the overall network:
 - $\hat{T}(s)$ satisfies the condition (3) of Theorem 1;
- 2) The local performance see [24]:
 - Synchronization *i.e.* the ramp tracking without velocity errors since $|S(j\omega)|$ has the positive slope $+40 \text{ dB/dec}$ in the low frequency range. It confirms and extends the result in [37] that the PI consensus algorithm is sufficient for the synchronization of the identical networked clocks without any symmetric assumption on the network topology.
 - Rejection of VCO input perturbations in all frequency range are at least -35 dB , see $|GS(j\omega)|$ transfer function in Fig.5. Furthermore, the $+20 \text{ dB/dec}$ slope in the low frequency range means that the system is able to reject the constant or slowly varying (drifting) VCO input perturbations. In other words, the non-identical central VCO frequencies (or clock speeds using the [37] terminology) which can be modeled as an input VCO perturbation, will not affect the final synchronization.
 - Rejection of PD and VCO output perturbations in high frequencies are at least -10 dB , see $|T(j\omega)|$ transfer function in Fig.5.

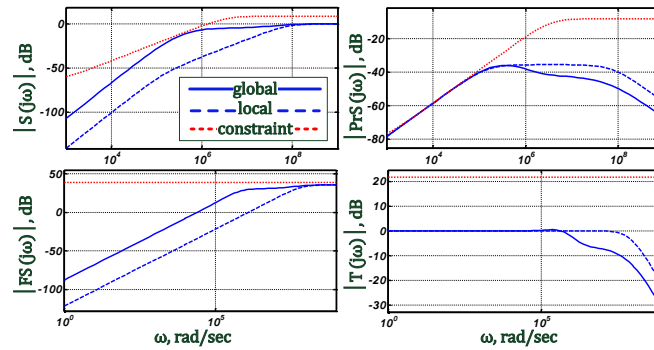


Fig. 5. Local sensitivity functions (dashed blue line) of a PLL node, the corresponding global performance transfer function (full blue line) and the weighting functions (dotted red line).

Global Performance discussion

The algorithm of the control law design *a priori* ensures the local performance only *i.e.* for a single PLL node. Since the global system is an interconnection of such PLL nodes, the global performance (of a PLL node in the network) can be degraded (Fig. 5). Depending on the kind of interconnections, the difference in the performance level can be significant. Nevertheless, the global performance can still inherit some desired properties from the local case. An analysis of the topology impact which is based on the Mason's Direct Rule can be found in [38, 39]. One can actually show that with $\gamma < 1$ the performance constraints are respected by some global transfer functions that take into account the network interconnection [33]. As it can be seen, the controller still ensures the synchronization without velocity error but with higher convergence time.

Finally, the bilinear continuous to discrete time transformation is performed to obtain the equivalent discrete control law. The proposed model herein is the real VLSI implementation of the network that takes into account the saturation of the phase detector and the filter outputs, the binary representation of the filter coefficients as well as the quantization of PD and DCO.

The synchronization efficiency with an external reference as in Fig.5 for a given randomly chosen initial state and central frequencies ($\pm 10\%$) is illustrated in Fig.7. The PD out-put saturation is observed for large error value (± 15). The sudden switches for tiny error values (± 1) is caused by the nature of the used bang-bang PD (for more details see [4]).

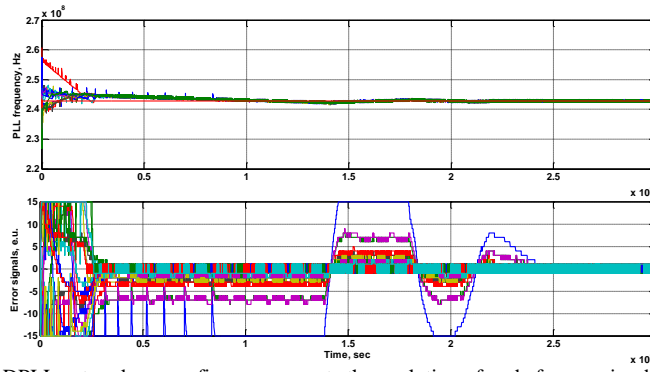


Fig. 6. Simulation results of the 4×4 ADPLL network, upper figure represents the evolution of node frequencies, bottom - the evolution of its relative phase error signal. Each PLL node is presented with a unique color.

VI. CONCLUSION

In this paper we have proposed a method of control law design for an LTI networked multi-agent system ensuring besides the local agent stability, the stability of the overall network as well as the local performance requirements. Although the global performance is degraded by the network interconnection, the shape of corresponding transfer functions is conserved ensuring the synchronization of the network. The next step in this work should introduce the global performance objectives during the design procedure. Another perspective is to examine through the μ -analysis tools the influence of uncertainties on the overall system stability and performance. The perturbations, unavoidable in practice, can include the models nonlinearities, delays or possible dispersions. This study should allow the realization of the solid-state PLL network VLSI circuit implementation.

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